

## PERSONAL INFORMATION



## Hutanu Ovidiu Emanuel

 Botosani (Romania)



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Sex Male | Date of birth 13 Nov 1989 | Nationality Romanian

## WORK EXPERIENCE

Apr 2015–Present

**Electronics Engineer**

Horia Hulubei National Institute for R&D in Physics and Nuclear Engineering (IFIN-HH)  
Atomistilor Street, no 407, Magurele, 077125 Ilfov (Romania)

[www.nipne.ro](http://www.nipne.ro)

I am part of the Romanian group of Romania-CERN collaboration, in NA62 experiment at CERN – Prévessin site, France. The main activity which I develop inside of this experiment is to develop and implement various electronic systems used for testing silicon photomultipliers (SiPM), and also as part of the data acquisition system. Another activity in which I am involved is PCB Design for ASICs based systems.

In the same collaboration Romania-CERN, I am part of Romanian group involved in LHCb experiment, at CERN Geneva site, Switzerland. The main activity which I develop inside of this experiment is PCB Designing for FPGA and ASICs based systems.

Primary tasks:

- Layout for printed board circuits (PCB);
- Front-end electronics design and maintenance;
- Custom linear and switching power supplies.

Other tasks:

- development and testing photodetectors circuits;
- development and testing power over ethernet (PoE) devices;
- assembling of electronics boards.

Business or sector Research & Development

## EDUCATION AND TRAINING

Sep 2018–Present

**Master studies**

"Politehnica" University of Bucharest (UPB), Bucharest (Romania)

[www.upb.ro](http://www.upb.ro)

Microsystems

Oct 2009–Jun 2014

**Electronics Engineer**

"Stefan cel Mare" University of Suceava (USV), Suceava (Romania)

[www.usv.ro](http://www.usv.ro)

Applied Electronics

Bachelor's degree

PERSONAL SKILLS

Mother tongue(s) Romanian

Foreign language(s)

	UNDERSTANDING		SPEAKING		WRITING
	Listening	Reading	Spoken interaction	Spoken production	
English	C1	C1	B2	B2	B2
French	A2	A2	A2	A2	B1

Levels: A1 and A2: Basic user - B1 and B2: Independent user - C1 and C2: Proficient user  
 Common European Framework of Reference for Languages

Communication skills Good communication skills

Organisational / managerial skills Good skills in working time organization and of work place organization.

Job-related skills Good skills in designing of layout for printed board circuits (PCB);  
 Design, implementation and testing of embedded systems, uC-based systems;  
 Good knowledge in applied electronics, analog and digital electronics;  
 Good skills of operation with measurement equipment.

Digital skills

SELF-ASSESSMENT				
Information processing	Communication	Content creation	Safety	Problem solving
Independent user	Independent user	Independent user	Proficient user	Independent user

Digital skills - Self-assessment grid

Driving licence AM, B1, B

ADDITIONAL INFORMATION

Field of interest Advanced layout design for printed board circuits (PCB)  
 Applied analog and digital electronics;  
 Linear and switching mode power supplies;  
 Microsystems engineering;  
 Automotive electronics systems;  
 Intelligent illumination system;  
 System for fast signals processing;  
 Microcontrollers and FPGA based systems;  
 Monitoring and Data Processing systems.